

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name,

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

CMOS INTEGRATED CIRCUIT DEVICE WITH LDD N-CHANNEL TRANSISTOR AND NON-LDD P-CHANNEL TRANSISTOR

the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint Theodore E. Galanthay, Reg. No. 24,122, Lisa K. Jorgenson, Reg. No. 34,845 and Irena Lager, Reg. No. 39,260 to prosecute this application and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

Please send all correspondence to:

Lisa K. Jorgenson
Reg. No. 34,845
SGS-Thomson Microelectronics, Inc.
1310 Electronics Drive
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(972) 466-7414

Inventor's Signature: Pervez A Sagar
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Inventor's Signature: M. Zamanian
Full Name of Second Joint Inventor: Mehdi Zamanian
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Inventor's Signature: _____
Full Name of Third Joint Inventor: Ravi Sundaresan
Date of Signature: _____
Residence and Post Office Address:
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Plano, TX 75025

Citizenship: India

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CMOS INTEGRATED CIRCUIT DEVICE WITH LDD N-CHANNEL TRANSISTOR AND NON-LDD P-CHANNEL TRANSISTOR

the specification of which was filed on 6/30/97 having been assigned Serial No. 08/885,636.

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I hereby appoint Lisa K. Jorgenson, Reg. No. 34,845, Mark McBurney, Reg. No. 33,114; Irena Lager, Reg. No. 39,260 and Theodore E. Galanthay, Reg. No. 24,122 to prosecute this application and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

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Citizenship: U.S.A.